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ANNUAL CONFERENCE

OF

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&

NATIONAL SYMPOSIUM

ON

Recent Developments in Applicable Mathematics & Information Technology

December 4-6, 2009

Organized by:

Jaypee Institute of Engineering & Technology
A.B. Road, Raghogarh
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JIET

Jaypee Institute of Engineering and Technology (JIET) is a constituent center of Jaypee University of Information Technology (JUIT), Himachal Pradesh (Set up by Act No. 14 of 2002) vide Extraordinary Gazette notification, govt. of HP dated May 23, 2002 and approved by the University Grants Commission under section 2(f) of the UGC act). This is one of the three institutions of higher learning in the field of Degree level Engineering education established under the Jai Prakash Sewa Sansthan < http:// www. jiit. ac. In / jiet/ jss.htm> (JSS) educational initiative, which recognizes the role of IT and emerging technologies in improving the quality of life of the people of India. JIET's location at Raghogarh, in Guna district is a well thought out process. This region, currently in a rural setting with strong agricultural activities is likely to grow as a major industrial hub. JIET is being developed as a major center to provide competent, well trained technical manpower to the region. The JIET campus on the national highway linking Agra to Mumbai is being developed on a 100 acres piece of land as modern institution of higher learning in the field of engineering and technology education. It aims to produce professionals who can be leaders in innovation, entrepreneurship, creativity and management. The first academic activities of JIET started in the year 2003. Presently, the institute offers 4 years (8 semester) B. Tech., M.Tech. and Doctoral Programmes in Chemical Engineering, Civil Engineering, Computer Science Engineering, Electronics and Communication Engineering, Mechanical Engineering and Applied Sciences.

VPI (Vijnana Parishad of India)

Vijnana Parishad of India is an International Society dedicated to the Application of Mathematics in all fields of Science & Technology and as such it has a crucial role to play because it is the only society in India explicitly devoted to this goal. Since very beginning Vijnana Parishad of India has a Reciprocity Agreement with American Mathematical Society. Vijnana Parishad of India is one of the five Indian Societies and thirteen Asian Societies which has Mutual Reciprocity Agreement with American Mathematical Society. The Parishad had organized 12 conferences at various places. Many eminent mathematicians had served the parishad in various capacities.

Theme of the Conference & Symposium

In recent years there has been exciting developments in all branches of mathematics and information technology. Rapid program in computer related fields based on mathematical sciences have created a lot of gales in the branches. The main objectives of the conference is to bring Mathematicians, Industrialists, Computer Scientists; IT specialists etc. together on one platform to interact with each other, share knowledge, exchange views, and discuss the topics of mutual interests and develop Inter disciplinary groups for fruitful applications of mathematics & IT to Industries. It also aims at presenting and discussing recent developments in mathematics & Information Tech.

Call for Papers

Original research papers, review of special topics, survey articles on recent developments in applicable mathematics & Information Technology are invited for presentation at the conference. Intended participants are invited to send the abstracts (not exceeding 250 words) of their papers to the below mentioned E-Mail address by August 16 and full paper by Sept.15. The academic program of the conference will consist of Plenary and Invited Talks, Symposia and Paper Presentations in the following areas:

The conference broad areas:
- Information Theory and Coding
- Mathematical Modeling & Computational methods
- Mathematical Models for Signal Processing
- Mathematical Biology and Bioinformatics
- Fluid Mechanics
- Advanced Numerical Techniques
- Applied Graph Theory and Network
- Data Mining and Knowledge Discovery
- Graphics, Entertainment, and Gaming
- Network Communication & Security
- Image Processing & Remote Sensing
- Novel Algorithmic Paradigm and Domain Specific Data Structures
- Distributed Processing & Grid Computing
- Service Oriented Architectures
- Artificial Intelligence & Neural Networks
- Embedded Systems & Fault tolerant Computing
- Software Engineering

- Applied Functional Analysis
- Operational Research & Survival Analysis
- Applied Statistical Methods
- Any relevant field applicable to Industry and Engineering Sciences.

Registration Fee

Registration	Students	Others
Early Bird Registration (Upto 31st Oct 2009)	Rs. 500/-	Rs.800/-
Late Registration (After 31st Oct 2009)	Rs. 700/-	Rs.1100/-

For participation in the conference the registration is compulsory. The Registered participants will be provided with free boarding and lodging from 3rd afternoon to 7th forenoon. Dec., 2009

Proceeding of the Conference

The papers presented at the conference will be considered for publication in the proceedings of the conference. Only referred and recommended papers will be published.

Important Dates

Deadlinefor Abstract Submission:August 31, 2009

DeadlineforFull Paper Submission: Sept. 15, 2009

Notification of Acceptance: October 15, 2009

Early Bird Registration: October 31, 2009

Address for Correspondence

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Registration form

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Of
Vijnana Parishad of India
&
National Symposium**

on
December 4th to 6th , 2009

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Fax : _____

Email : _____

Accommodation required: Yes/No (If Yes)

From_____To_____

Details of DD :

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Note: Demand draft may be issued in the name of JIET Payable at Guna. Photocopy of this form can also be used.